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APPLICANTS

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** CONTINUING DATA ***** NONE, NMD

** FOREIGN APPLICATIONS ***** VERIFIED, NMD
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <u>Amogh</u> NMD Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 14	TOTAL CLAIMS 25	INDEPENDENT CLAIMS 16
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TITLE
 Semiconductor integrated circuit designing apparatus, semiconductor integrated circuit designing method, semiconductor integrated circuit manufacturing method, and readable recording media

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